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PATENT APPLICATION

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U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub-</u> <u>Class</u>
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FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
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OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>(Including Author, Title, Date, Pertinent Pages, Etc.)</u>
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<u>NB</u>	_____	Preliminary Data Sheet, Microelectronics Group, Lucent Technologies Bell Labs Innovations, "TTSI4K32T 4096-Channel, 32-Highway Time Slot Interchanger", February 1999, 4 pages.
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<u>NB</u>	_____	Xilinx Inc. Product Specification, Logicare Asynchronous FIFO V3.0, November 3, 2000, pages 1-12.
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Examiner: My Ut

Date Considered: 9/27/05